

### Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the Application.

### Listing of Claims:

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1. (Currently amended) A computer implemented method for determining system information, wherein the system is comprised of at least one host adaptor, at least one switch, and at least one Input/Output (I/O) device, wherein a path in the system from one host adaptor to the I/O device includes as path components one host adaptor, one switch, one I/O storage device, a first link between the host adaptor and the switch and a second link between the switch and the I/O-storage device, comprising:  
determining component information on host adaptor, switch, and I/O device components in a network system;  
adding the determined component information to a configuration file providing configuration information on the network system;  
for each determined host adaptor, performing:
    - (i) determining, from the component information, information on the first link between the host adaptor and the switch;
    - (ii) determining, from the component information, information on the I/O device to which the host adaptor communicates;
    - (iii) determining the second link between the I/O device and the switch; and
    - (iv) adding information on the first and second link to the configuration file.
  2. (Original) The method of claim 1, wherein the second link is determined by using the determined information on the first link and the I/O device to which the host adaptor communicates.
  3. (Original) The method of claim 1, further comprising:

receiving a request from an application program for configuration information on at least one component in the system;  
querying the configuration file to determine the requested configuration information; and  
returning the requested configuration information to the application program.

4. (Original) The method of claim 1, wherein the component information includes the address of each component in the system.

5. (Original) The method of claim 4, wherein the component information includes a loop address of each I/O device connecting to a loop that also connects to the switch, wherein the component information further includes information on multiple loops to which the switch connects and for each loop, the address of all the devices that are attached to the loop, wherein determining the second link further comprises:  
determining one I/O device having a loop address that matches the loop address of one device attached to the loop to which the switch connects, wherein the second link includes the loop to which the determined I/O device and switch connect.

6. (Currently amended) The method of claim 5, wherein the switch includes multiple destination ports and initiator ports, wherein the initiator ports connect to host adaptors and the destination ports connect to I/O-storage devices, wherein the first link includes the initiator port and wherein the second link includes the destination port.

7. (Original) The method of claim 4, wherein the switch is comprised of multiple initiator and destination ports, wherein the component information indicates the address of each initiator and destination port in the switch, wherein the information on the first link indicates the initiator port on the switch to which the host adaptor connects and wherein the information on the second link indicates the destination port on the switch to which the I/O device connects, wherein at least one path includes one destination port and initiator port in the switch.

8. (Original) The method of claim 7, wherein the address of each initiator port comprises the address of the host adaptor connected to the initiator port, wherein determining the first link further comprises:  
determining the host adaptor having the same address as the address of one initiator port,  
wherein the first link comprises a connection between the host adaptor and  
initiator port having the same address.

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9. (Original) The method of claim 7, wherein a plurality of destination ports connect to loops, wherein a plurality of devices are capable of being attached to the loop and wherein each attached device and the destination port have a loop address on the loop, wherein a plurality of I/O devices connect to the loops, wherein the component information indicates the loop address of the I/O devices connected to the loops, and wherein determining the second link further comprises:  
for each initiator port, performing:  
determining one destination port the initiator port is capable of accessing; and  
determining one I/O device having a loop address that matches the loop address of one of the devices attached to the loop to which the determined destination port is attached, wherein the second link includes the loop to which the determined I/O device and determined destination port are attached.

10. (Original) The method of claim 9, wherein the component information includes a physical path address for each host adaptor and I/O device, wherein the address of each initiator port comprises the address of the host adaptor connected to the initiator port, further comprising:  
determining the host adaptor having the same address as the address of one initiator port,  
wherein the first link comprises a connection between the host adaptor and  
initiator port having the same address; and  
determining one I/O device having a same physical path address as the determined host adaptor, wherein the determined host adaptor transfers data to the I/O device

having the same physical path address, wherein the component information associates the destination port with the initiator port having the same address as the host adaptor that has the same physical path address as the I/O device to which the destination port connects.

11. (Original) The method of claim 7, wherein the switch implements the Fibre Channel protocol.

12. (Original) The method of claim 1, wherein the I/O device comprises a storage device.

13. (Currently amended) A system for determining network information, wherein the network is comprised of at least one host adaptor, at least one switch, and at least one Input/Output (I/O) device, wherein a path in the network from one host adaptor to the I/O device includes as path components one host adaptor, one switch, one I/O-storage device, a first link between the host adaptor and the switch and a second link between the switch and the I/O-storage device, comprising:

means for determining component information on host adaptor, switch, and I/O device components in the network;

means for adding the determined component information to a configuration file providing configuration information on the network system;

means for performing, for each determined host adaptor:

- (i) determining, from the component information, information on the first link between the host adaptor and the switch;
- (ii) determining, from the component information, information on the I/O device to which the host adaptor communicates;
- (iii) determining the second link between the I/O device and the switch; and
- (iv) adding information on the first and second link to the configuration file.

14. (Original) The system of claim 13, wherein the second link is determined by using the determined information on the first link and the I/O device to which the host adaptor communicates.

15. (Original) The system of claim 13, further comprising:

means for receiving a request from an application program for configuration information on at least one component in the system;

means for querying the configuration file to determine the requested configuration information; and

means for returning the requested configuration information to the application program.

16. (Original) The system of claim 13, wherein the component information includes the address of each component in the system.

17. (Original) The system of claim 16, wherein the component information includes a loop address of each I/O device connecting to a loop that also connects to the switch, wherein the component information further includes information on multiple loops to which the switch connects and for each loop, the address of all the devices that are attached to the loop, wherein the means for determining the second link further performs:  
determining one I/O device having a loop address that matches the loop address of one device attached to the loop to which the switch connects, wherein the second link includes the loop to which the determined I/O device and switch connect.

18. (Currently amended) The system of claim 17, wherein the switch includes multiple destination ports and initiator ports, wherein the initiator ports connect to host adaptors and the destination ports connect to I/O-storage devices, wherein the first link includes the initiator port and wherein the second link includes the destination port.

19. (Original) The system of claim 16, wherein the switch is comprised of multiple initiator and destination ports, wherein the component information indicates the address of each

initiator and destination port in the switch, wherein the information on the first link indicates the initiator port on the switch to which the host adaptor connects and wherein the information on the second link indicates the destination port on the switch to which the I/O device connects, wherein at least one path includes one destination port and initiator port in the switch.

AD 20. (Original) The system of claim 19, wherein the address of each initiator port comprises the address of the host adaptor connected to the initiator port, wherein the means for determining the first link further performs:  
determining the host adaptor having the same address as the address of one initiator port,  
wherein the first link comprises a connection between the host adaptor and initiator port having the same address.

21. (Original) The system of claim 19, wherein a plurality of destination ports connect to loops, wherein a plurality of devices are capable of being attached to the loop and wherein each attached device and the destination port have a loop address on the loop, wherein a plurality of I/O devices connect to the loops, wherein the component information indicates the loop address of the I/O devices connected to the loops, and wherein the means for determining the second link further performs for each initiator port:  
determining one destination port the initiator port is capable of accessing; and  
determining one I/O device having a loop address that matches the loop address of one of the devices attached to the loop to which the determined destination port is attached,  
wherein the second link includes the loop to which the determined I/O device and determined destination port are attached.

22. (Original) The system of claim 21, wherein the component information includes a physical path address for each host adaptor and I/O device, wherein the address of each initiator port comprises the address of the host adaptor connected to the initiator port, further comprising:

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means for determining the host adaptor having the same address as the address of one initiator port, wherein the first link comprises a connection between the host adaptor and initiator port having the same address; and

means for determining one I/O device having a same physical path address as the determined host adaptor, wherein the determined host adaptor transfers data to the I/O device having the same physical path address, wherein the component information associates the destination port with the initiator port having the same address as the host adaptor that has the same physical path address as the I/O device to which the destination port connects.

23. (Original) The system of claim 19, wherein the switch implements the Fibre Channel protocol.

24. (Original) The system of claim 13, wherein the I/O device comprises a storage device.

25. (Currently amended) An article of manufacture implementing code to determine system information, wherein the system is comprised of at least one host adaptor, at least one switch, and at least one Input/Output (I/O) device, wherein a path in the system from one host adaptor to the I/O device includes as path components one host adaptor, one switch, one I/O-storage device, a first link between the host adaptor and the switch and a second link between the switch and the I/O-storage device, by:

determining component information on host adaptor, switch, and I/O device components in a network system;

adding the determined component information to a configuration file providing configuration information on the network system;

for each determined host adaptor, performing:

(i) determining, from the component information, information on the first link between the host adaptor and the switch;

- (ii) determining, from the component information, information on the I/O device to which the host adaptor communicates;
- (iii) determining the second link between the I/O device and the switch; and
- (iv) adding information on the first and second link to the configuration file.

AD 26. (Original) The article of manufacture of claim 25, wherein the second link is determined by using the determined information on the first link and the I/O device to which the host adaptor communicates.

27. (Original) The article of manufacture of claim 25, further comprising:  
receiving a request from an application program for configuration information on at least one component in the system;  
querying the configuration file to determine the requested configuration information; and  
returning the requested configuration information to the application program.

28. (Original) The article of manufacture of claim 25, wherein the component information includes the address of each component in the system.

29. (Original) The article of manufacture of claim 28, wherein the component information includes a loop address of each I/O device connecting to a loop that also connects to the switch, wherein the component information further includes information on multiple loops to which the switch connects and for each loop, the address of all the devices that are attached to the loop, wherein determining the second link further comprises:  
determining one I/O device having a loop address that matches the loop address of one device attached to the loop to which the switch connects, wherein the second link includes the loop to which the determined I/O device and switch connect.

30. (Currently amended) The article of manufacture of claim 29, wherein the switch includes multiple destination ports and initiator ports, wherein the initiator ports connect



to host adaptors and the destination ports connect to ~~I/O-storage~~ devices, wherein the first link includes the initiator port and wherein the second link includes the destination port.

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31. (Original) The article of manufacture of claim 28, wherein the switch is comprised of multiple initiator and destination ports, wherein the component information indicates the address of each initiator and destination port in the switch, wherein the information on the first link indicates the initiator port on the switch to which the host adaptor connects and wherein the information on the second link indicates the destination port on the switch to which the I/O device connects, wherein at least one path includes one destination port and initiator port in the switch.

32. (Original) The article of manufacture of claim 31, wherein the address of each initiator port comprises the address of the host adaptor connected to the initiator port, wherein determining the first link further comprises:  
determining the host adaptor having the same address as the address of one initiator port,  
wherein the first link comprises a connection between the host adaptor and initiator port having the same address.

33. (Original) The article of manufacture of claim 31, wherein a plurality of destination ports connect to loops, wherein a plurality of devices are capable of being attached to the loop and wherein each attached device and the destination port have a loop address on the loop, wherein a plurality of I/O devices connect to the loops, wherein the component information indicates the loop address of the I/O devices connected to the loops, and wherein determining the second link further comprises:  
for each initiator port, performing:  
determining one destination port the initiator port is capable of accessing; and  
determining one I/O device having a loop address that matches the loop address of one of the devices attached to the loop to which the determined destination port is

attached, wherein the second link includes the loop to which the determined I/O device and determined destination port are attached.

34. (Original) The article of manufacture of claim 33, wherein the component information includes a physical path address for each host adaptor and I/O device, wherein the address of each initiator port comprises the address of the host adaptor connected to the initiator port, further comprising:
- determining the host adaptor having the same address as the address of one initiator port, wherein the first link comprises a connection between the host adaptor and initiator port having the same address; and
- determining one I/O device having a same physical path address as the determined host adaptor, wherein the determined host adaptor transfers data to the I/O device having the same physical path address, wherein the component information associates the destination port with the initiator port having the same address as the host adaptor that has the same physical path address as the I/O device to which the destination port connects.

35. (Original) The article of manufacture of claim 31, wherein the switch implements the Fibre Channel protocol.

36. (Original) The article of manufacture of claim 25, wherein the I/O device comprises a storage device.
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